

FIG. 1

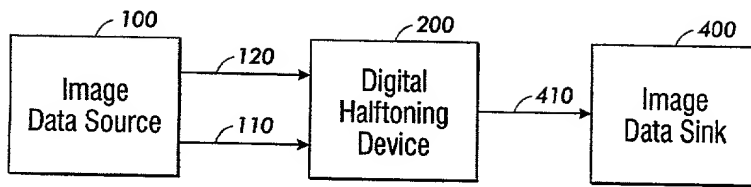


FIG. 2

FIG. 2 is a block diagram of a digital halftoning system. The system includes an image data source (100) which provides image data to a digital halftoning device (200). The digital halftoning device (200) processes the image data and outputs the processed data to an image data sink (400). The image data source (100) is connected to the digital halftoning device (200) via a data path (110). The digital halftoning device (200) is connected to the image data sink (400) via a data path (410). The image data source (100) also provides a control signal (120) to the digital halftoning device (200).

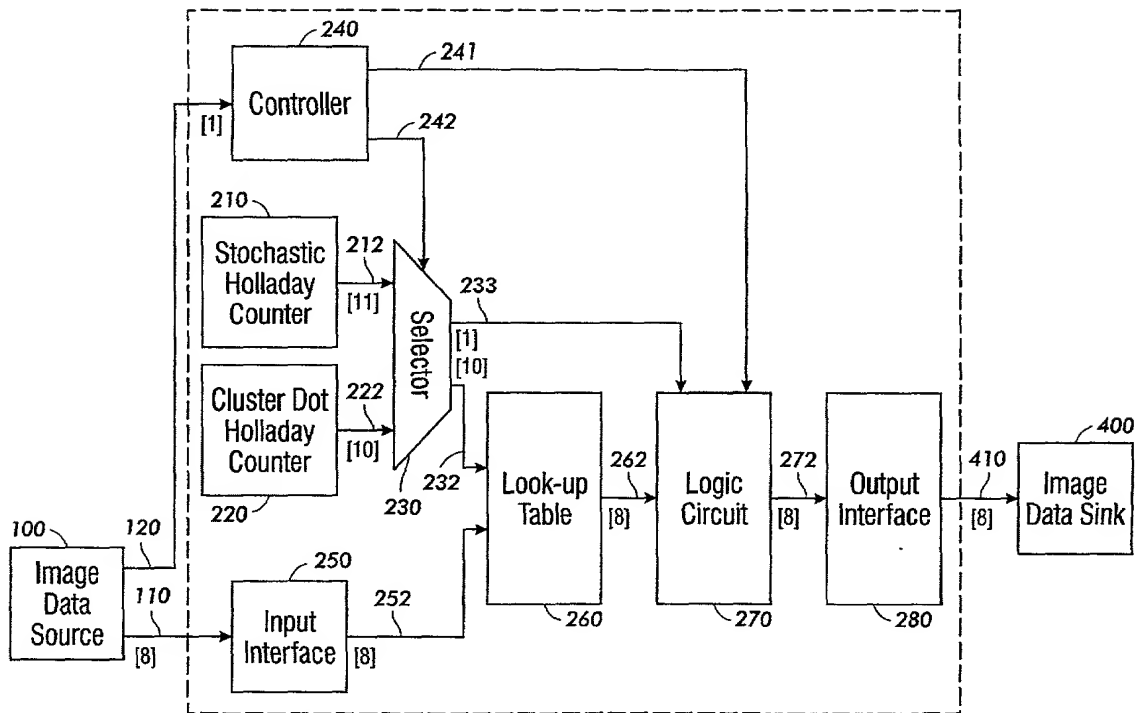


FIG. 3

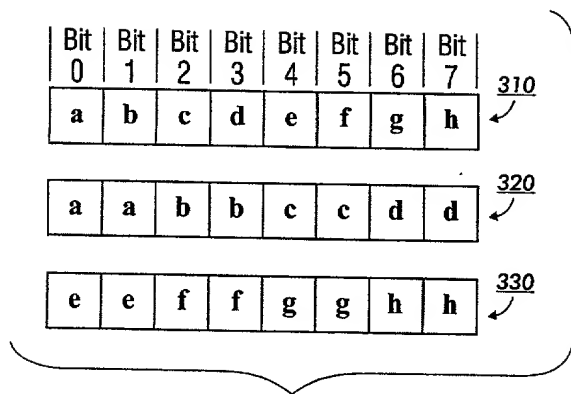


FIG. 4

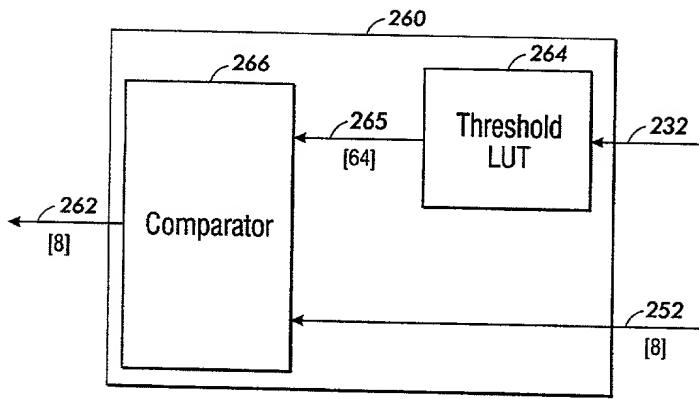


FIG. 5

FIG. 5 is a block diagram of a circuit.

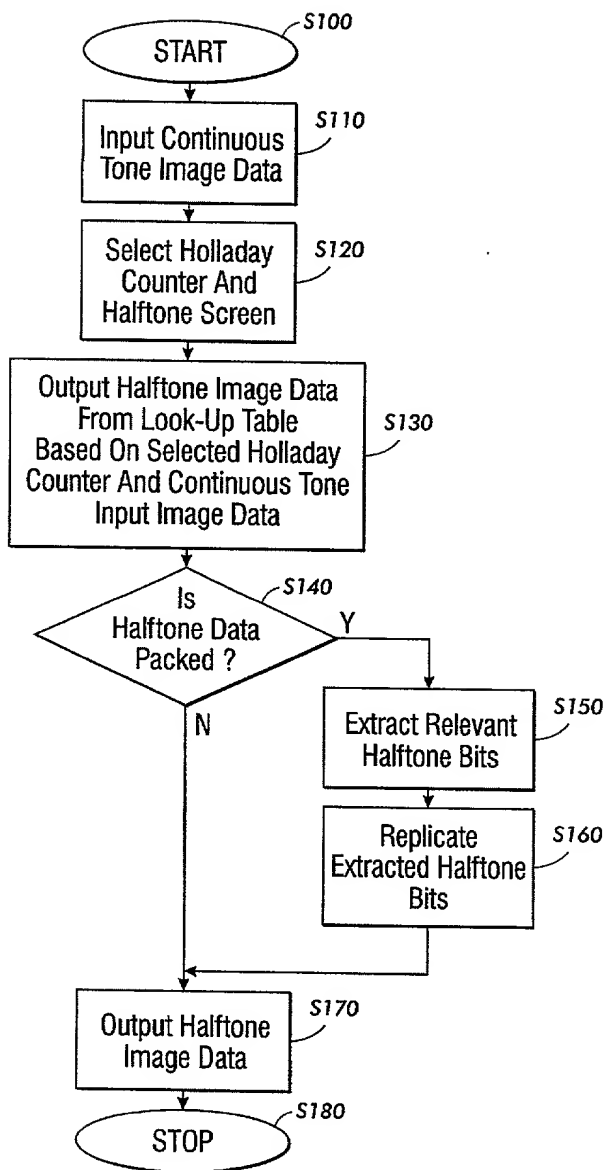


FIG. 6

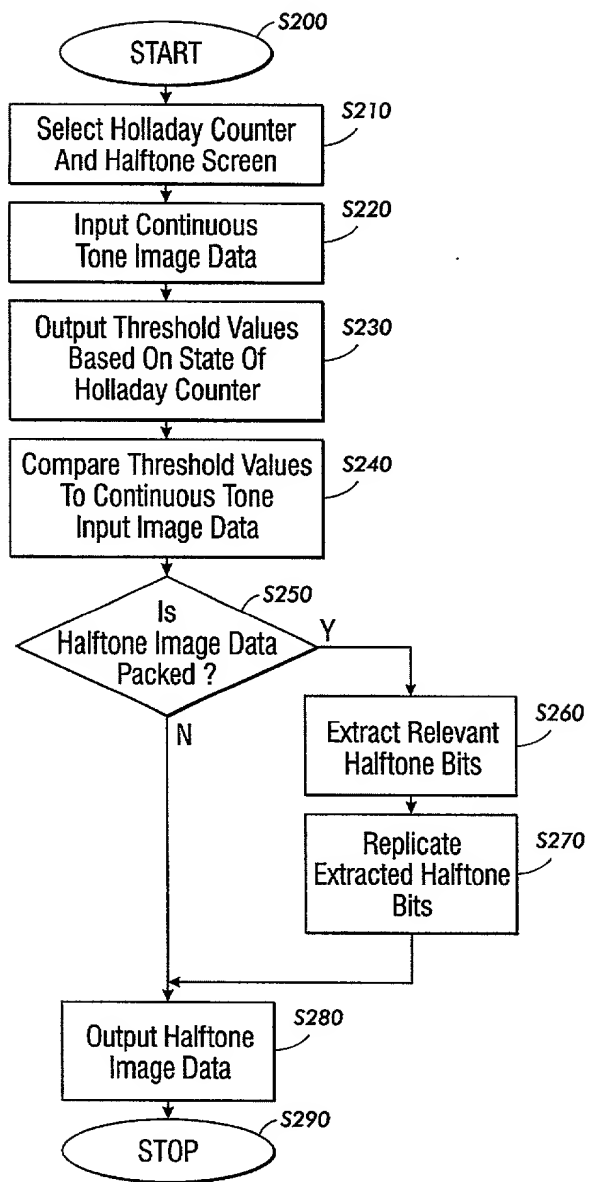


FIG. 7